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Serial No. : 10/521,931
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Attorney's Docket No.: 14603-009US1
Client Ref. No.: P2002,0626 US N

Amendments to the Drawings:

The attached replacement sheet of drawings replace the original sheets.

In Figures 1-4 the hand drawn component labels have been replaced with type-written labels as requested by the examiner.

Attachments following last page of this Amendment:

Replacement Sheets (2 pages)

REMARKS

Claims 1-11 and 19-21 are pending in this application. Of these, claims 1 and 6 are independent. Favorable reconsideration and further examination are respectfully requested.

Drawings

In response to the Examiner's objection to the figures, new drawings are being submitted herewith.

Rejection under 35 U.S.C. 102(a)

The Examiner rejected claims 1-11 under 35 U.S.C. 102(a) as being anticipated by Jain (US 6,615,237) or under 103 as being obvious in view of Jain.

Claim 1 relates to a circuit array for controlling operation of two loads that operate with a rectified AC voltage. Claim 1 recites a semiconductor switch on a circuit path that includes the two loads and a control unit to generate a switch control signal that controls the semiconductor switch.

As shown in FIG. 1 of Jain, Jain describes a modulation control circuit for providing a voltage to a single load 126. Jain states, that "a DC output [] voltage 128 across the third capacitor 124 is connected to a load 126 (shown in dashed lines)."¹ As such, Jain fails to disclose or suggest "a circuit array for controlling operation of two loads that operate with a rectified AC voltage" as recited in the applicant's claim 1.

The examiner appears to equate the switches 108 and 106 which are connected to the outputs 110 and 122 to the applicant's "two loads."² Jain uses two semiconductor switches 108 and 106 rectify a voltage from the secondary windings 106B of a transformer 106 and to control the voltage applied to the single load 126. Signals 110 and 122 are control signals which are provided to the gate terminals of the two semiconductor switches 108 and 116 in the synchronous rectifier. For example, Jain states "it is an object of the invention to provide

¹ Col. 1, lines 49-51.

² Office action, page 3.

appropriate gating signals for the controlled rectifier switches...”³ The gate terminals of the two semiconductor switches 108 and 106 can not be equivalent to the applicant's “loads” because the gate terminals of the transistors are controlled only by a voltage and do not require any current. Further, the gate terminals of the two semiconductor switches 108 and 106 can not be equivalent to the applicant's “loads” because the switches 108 and 106 are part of Jain's synchronous rectifier that is used to generate a rectified voltage. As such, switches 108 and 106 do not “operate with a rectified voltage” as required by claim 1.

Claims 2-5 depend from claim 1 and are patentable for at least the reasons claim 1 is patentable.

Claim 6 recites “a rectifier that is connected to the input and that provides the voltage to the loads, the voltage being generated from the AC voltage, wherein the rectifier comprises an open bridge circuit, and wherein the voltage comprises different half waves of the AC voltage that are applied to different loads.” As described above, Jain describes a circuit configured to power a single load. Since Jain does not describe powering multiple loads, Jain does not further describe or suggest using a rectifier configured such that the different half waves of the AC voltage are applied to the different loads.

Claims 7-11 depend from claim 6 and are patentable for at least the reasons claim 6 is patentable.

Summary

It is believed that all of the pending claims have been addressed. However, the absence of a reply to a specific rejection, issue or comment does not signify agreement with or

³ Col. 3, lines 28-31.

concession of that rejection, issue or comment. In addition, because the arguments made above may not be exhaustive, there may be reasons for patentability of any or all pending claims (or other claims) that have not been expressed. Finally, nothing in this paper should be construed as an intent to concede any issue with regard to any claim, except as specifically stated in this paper, and the amendment of any claim does not necessarily signify concession of unpatentability of the claim prior to its amendment.


In view of the foregoing amendments and remarks, Applicants respectfully submit that the application is in condition for allowance, and such action is respectfully requested at the Examiner's earliest convenience.

Applicants' undersigned attorney can be reached at the address shown below. All telephone calls should be directed to the undersigned at 617-956-5986.

Please apply any other charges or credits to deposit account 06-1050.

Respectfully submitted,

Date: 2/26/08



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